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		TRAIR F	RADEMOS	<u>anua/ry</u>	5, 2007	<u>/ </u>	(Date)
APPLICATION NO.	FILING DATE		FIRST NAMED INVENTOR	2	ATTO	RNEY DOCKET NO.	CONFIRMATION NO.
10/722,814	10/722,814 11/26/2003		Cha Deok Dong			29936/39765 9355	
TITLE OF INVENTION: METHOD OF FORMING GATE ELECTRODE IN SEMICONDUCTOR DEVICE							
01/09/2007 EHAILE2							
				61	/09/2007 EH	VILES COORDAS	
APPLN. TYPE	SMALL ENTITY	ISSUE FEE DUE	PUBLICATION FEE DUE		DISSUE FEE	TOTAL FEE(S) DUI	300 DATE DUE
nonprovisional	NO	\$1400	\$300	9.	\$00:8001	\$1700	02/26/2007
EXAM	INER	ART UNIT	CLASS-SUBCLASS	7			
DEO, DUY VU NGUYEN		1765	438-257000	3			•
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Hynix Semiconductor Inc. Kyungki-do, Republic of Korea							
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